



2615
PATENT
B208-975

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Izumi Matsui
Serial No. : 09/128,251
For : SIGNAL PROCESSING DEVICE
Filed : August 3, 1998
Examiner : P. Chieu
Art Unit : 2615

#5/A
Rose
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Technology Center 2600

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

AMENDMENT UNDER 37 CFR 1.121(a)

*AWC
1/22/02*
In response to the Office Action mailed May 8, 2001, please amend the above application as is set forth below.

In the Specification

Page 1, line 2, delete in its entirety and insert:

--SIGNAL PROCESSING DEVICE FOR PROCESSING VIDEO SIGNAL
INFORMATION BY USING MEMORY --.

Rewrite the paragraph starting at page at page 10, line 15 and ending at page 10, line 26, as follows:

As shown in Fig. 2, in the VTR according to the embodiment, there is provided the memory 125 which is commonly accessed by each of the input/output processing circuits, the compression/expansion circuits, the error correction processing circuits, and recording/reproduction

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to:
Assistant Commissioner for Patents, Washington, D.C. 20231, on:

November 8, 2001

November 8, 2001

Date of Signature

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Signature

AI
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